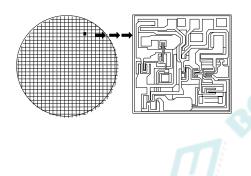
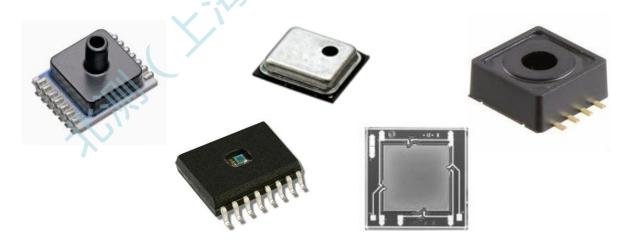
AEC - Q103 - 002 Rev-March 1, 2019



FAILURE MECHANISM BASED STRESS TEST QUALIFICATION Mgosoft Page Security

MICRO ELECTRO-MECHANICAL SYSTEM (MEMS) PRESSURE SENSOR DEVICES



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Acknowledgment

Any document involving a complex technology brings together experience and skills from many sources. The Automotive Electronics Council would especially like to recognize the following significant contributors to the revision of this document:

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AEC - Q103 - 003 Rev-February 14, 2019

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FAILURE MECHANISM BASED STRESS TEST QUALIFICATION FOR MICRO ELECTRO-MECHANICAL SYSTEM (MEMS) PRESSURE SENSOR DEVICES

1. SCOPE

This document contains a set of failure mechanism based stress tests specific to the Micro Electro-Mechanical System (MEMS) Pressure Sensor technologies listed in Section 1.1.1 below. This document shall be used in conjunction with AEC-Q100. The circuit elements of MEMS devices are susceptible to the same failure mechanisms as standard IC's, thus must meet the requirements defined in AEC-Q100. The MEMS portion of these devices, including circuit and package interactions, must meet the requirements defined herein.

The objective is to precipitate failures in an accelerated manner compared to use conditions, or to simulate extreme events to draw out design or intrinsic process deficiencies. This set of tests should not be used indiscriminately. Each qualification project should be examined for:

- a. Any potential new and unique failure mechanisms
- b. Any situation where these tests/conditions may induce failures that would not be seen in the application
- c. Any extreme use condition and/or application that could adversely reduce the acceleration

Use of this document does not relieve the MEMS supplier of their responsibility to meet their own company's internal qualification program. In this document "user" is defined as all customers using a device qualified per this specification. User specific requirements will need to be considered in addition to these recommendations. The liser is responsible to confirm and plaidate at qualification data that substantiates comparable to this document. Supplier usage of the device temperature grades as stated in this specification in their part information is strongly encouraged.

1.1 Purpose

The purpose of this specification is to determine that a MEMS pressure sensor device is capable of passing the specified stress tests and thus can be expected to give a certain level of quality/reliability in the application.

1.1.1 MEMS Pressure Sensor Technologies

The MEMS Pressure Sensor device technologies considered during the development of this document include:

- Polysilicon surface micro-machined
- Single crystal silicon Deep Reactive Ion Etching (DRIE)
- Bulk micro-machined
- Capping processes including:
 - Glass frit
 - Eutectic bonding
 - Fusion bonding
 - Anodic bonding

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1.1.2 MEMS Pressure Sensor Types and Packaging

MEMS pressure sensor device types included in the scope of this document are as follows:

- A pressure sensing element integrated into a signal conditioning IC ("co-integrated") mounted in an open cavity (gel covered or gel free) package
- A stacked die/side-by-side configuration where a pressure sensing element is mounted on/next to a signal conditioning IC in open cavity (gel covered or gel free) package
- A pressure sensing element mounted into a pre-mold cavity (gel covered or gel free) after overmolding of the signal conditioning IC
- A pressure sensing element mounted into a pre-mold cavity (gel covered or gel free) after package molding
- A pure pressure sensing element consisting of an unpackaged silicon micro-machined piezo-resistive pressure sensing element (i.e., bare die delivery)

MEMS pressure sensor packaging includes, but is not limited to, the following:

- Non-Hermetic Cavity Package
- Non-Hermetic Leadframe Cavity Package
- Overmolded Leadframe Package
- Overmolded Laminate Package

1.2 Reference Documents

The current revision of the referenced documents will be in effect at the date of agreement to the qualification plans. Subsequent qualification plans will automatically use updated revisions of these references of currents.

1.2.1 Automotive

AEC-Q100 Failure Mechanism Based Stress Test Qualification for Integrated Circuits

1.2.2 Military

MIL-STD-202 Test Method Standard: Electronic and Electrical Component
MIL-STD-883 Test Method Standard: Microcircuits

1.2.3 Industrial

JEDEC JESD22

Reliability Test Methods for Packaged Devices

Testing in a saturated atmosphere in the presence of sulfur dioxide
EN 60068-2-60

ISO 16750-5

Reliability Test Methods for Packaged Devices

Testing in a saturated atmosphere in the presence of sulfur dioxide
Environmental testing - Flowing mixed gas corrosion test
Road vehicles - Environmental conditions and testing for electrical and electronic equipment – Part 5: Chemical loads

1.3 Definitions

1.3.1 AEC Q103-002 Qualification

Successful completion and documentation of the test results from requirements outlined in this document and AEC-Q100 document allows the supplier to claim that the part is "AEC-Q103-002 qualified".

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1.3.2 AEC Certification

Note that there are no "certifications" for AEC-Q103-002 qualification and there is no certification board run by AEC to qualify parts. Each supplier performs their qualification to AEC standards, considers customer requirements and submits the data to the customer to verify compliance to AEC-Q103-002.

1.3.3 Definition of MEMS Pressure Sensor Part Operating Temperature Grades:

The part operating temperature grades are defined in Table 1 of AEC-Q100. Additional temperature grades applicable to MEMS Pressure Sensor devices are defined in Table 1A below:

Table 1A: Additional MEMS Pressure Sensor Part Operating Temperature Grades

Grade	Ambient Operating Temperature Range
0A	-40°C to +165°C
0B	-40°C to +175°C

All automotive grades as defined in AEC-Q100 apply; the above grades are only needed if ambient operating temperature range exceeds AEC-Q100 grade zero requirements. For all biased tests from Table 2 of this document and Table 2 of AEC-Q100, the junction temperature of the MEMS pressure sensor device during stressing should be equal to or greater than the hot temperature for that grade.

If the minimum or maximum ambient temperature as specified in the supplier datasheet cannot be found in Table 1. If this to unant or Table 1 or AEC 2160 then the next more challenging part operating temperature grade must be selected. Exceptions include the following:

- If the hot temperature of a chosen part operating temperature grade exceeds the allowed maximum temperature specified in the supplier datasheet, then testing should be limited to the maximum datasheet value. This applies only to biased tests from Table 2 of this document (e.g., PrHTOL, B_PPrTC) and biased tests from Table 2 of AEC-Q100 (e.g., HTOL, ELFR, PTC). Actual tests and maximum ambient temperature used shall be per mutual agreement between user and supplier.
- Endpoint hot temperature for Pre- and Post-Stress Function/Parameter Verification testing should be limited to the maximum ambient operating temperature specified in the supplier datasheet.

1.3.4 Definition of MEMS Pressure Sensor Part Mechanical Grade:

The part mechanical grades for MEMS pressure sensors are defined in Table 1B below:

Table 1B: MEMS Pressure Sensor Part Mechanical Grades

Grade	Application Requirement
M1	Pressure Sensor – General
M2	Tire Pressure Monitoring System (TPMS) – Rim Mounted

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2. GENERAL REQUIREMENTS

MEMS Pressure Sensor device qualification shall be compliant to AEC-Q100 with additional requirements as defined herein.

2.1 Precedence of Requirements

In the event of conflict in the requirements of this standard and those of any other documents, the following order of precedence applies:

- The purchase order (or master purchase agreement terms and conditions)
- b. The (mutually agreed) individual device specification
- c. This document
- d. AEC-Q100
- e. The reference documents in Section 1.2 of this document
- f. The supplier's datasheet

For the device to be considered a qualified part per this specification, the purchase order and/or the individual device specification cannot waive or detract from the requirements of this document.

2.5 Definition of Test Failure After Stressing

In addition to AEC-Q100 requirements, Test Group PS shall be used to disposition rejects from AEC-Q100 temperature cycling or accelerated moisture stresses that are not accelerated failure mechanisms.

3. QUALIFICATION AND REQUIRED TO SECURITY

3.1 Qualification of a New MEMS Pressure Sensor Device

Test Group PS provides guidance for the disposition of rejects specific to the MEMS element or MEMS to package interactions that occur due to the physical overstress inherent in accelerated temperature cycling and moisture tests at permanent or cycled pressure impact. This test group does not apply to the accelerated failure mechanisms for which the AEC-Q100 stress conditions are derived, such as corrosion, and the supplier must demonstrate that the circuit and package are free of these mechanisms.

3.2 Criteria for Passing Requalification

All requalification failures shall be analyzed for root cause. Only when corrective and preventative actions are in place and proven effective, the MEMS pressure sensor device may then be considered AEC-Q103-002 qualified again.

4. QUALIFICATION TESTS

4.1 General Tests

In addition to well-known IC failure mechanisms, MEMS pressure sensor devices require specific qualification tests to verify performance of both the MEMS die and the packaging in an application environment taking into account mutual interactions, including environmental and functional loading. Unique qualification tests and/or test sequences are defined for MEMS pressure sensor devices by the presence of a pressure port and exposure of the pressure membrane to environmental influences. Stress tests have been defined on the basis of interactions of environmental and functional loads of MEMS pressure sensor devices (see Figure 1)÷.

- a. Environmental loads include pressure, temperature, and humidity.
- b. Functional loads include mechanical and chemical.

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- c. The set of loads and diverse interactions of their states (e.g., constant, cycled/pulsed, rapid change, shock) define the unique qualification tests and their sequences:
 - Pressure load states define the pressure life tests, pressure pulsed tests, and proof/burst tests:
 - Interaction between pressure, temperature, humidity, and chemical loads defines preconditioning before pressure tests and chemical tests.
 - Interaction between pressure, temperature, and humidity makes HAST and UHST more preferable tests than THB and AC.
 - Chemical load states define the chemical tests such as corrosive atmosphere, chemical resistance, salt immersion, etc.:
 - Interaction between temperature, humidity, chemical, and mechanical loads defines the internal visual inspection and wire bond pull testing performed post-chemical and post-mechanical tests.

The stress test requirement flow for qualification of a new MEMS pressure sensor device is shown in Figure 2. This specification defines the requirements for the qualification of MEMS pressure sensor devices. It is to be used in conjunction with AEC-Q100, rather than in lieu of. AEC-Q100 shall be used to qualify the active circuitry and basic package integrity of the MEMS pressure sensor device. Qualification tests and/or test sequences specific to MEMS pressure sensor devices are detailed in Figure 2 and Table 2A. Table 2B lists the AEC-Q100 tests updated to address MEMS pressure sensor device failure mechanisms.

Not all AEC-Q100 tests apply to all MEMS pressure sensor devices. For example, Constant Acceleration (CA, test #G3) as Pre-conditioning before Mechanical Shock (MS, test #G1) and Variable Frequency Vibration (VFV, test #G2) is only applicable to TPMS devices. The applicable tests for the darticular device type are in lighted in the Note" column of Tables 2A and 2B. The "Additional Requirements" tolumn of Tables 2 and 2B as a server to highlight test requirements that supersede those described in the referenced test method. Any unique qualification tests or conditions requested by the user and not specified in this document shall be negotiated between the supplier and user requesting the test. The Target Failure Mechanism column serves to provide guidance as to the rationale for the requirement.

4.2 Device Specific Tests

MEMS pressure sensor device specific tests shall be performed in accordance with Section 4.2 of AEC-Q100. In addition, the following tests must be performed on the specific MEMS pressure sensor device to be qualified in a given package. Generic data is not allowed for these tests. Device specific data, if it already exists, is acceptable.

- 1. Highly-Accelerated Temperature and Humidity Stress Test (Test #A2)
- 2. Pressure & High Temperature Operating Life Test (Test #PS1)
- 3. Biased Pulsed Pressure Temperature Cycling (Test #PS2)
- 4. Burst Pressure (Test #PS7)
- 5. Proof Pressure (Test #PS8)
- Other Tests A user may require other tests in lieu of generic data based on his experience with a particular supplier (e.g., Tests #G1-#G3 for TPMS depends on whether rim or tire mounted).

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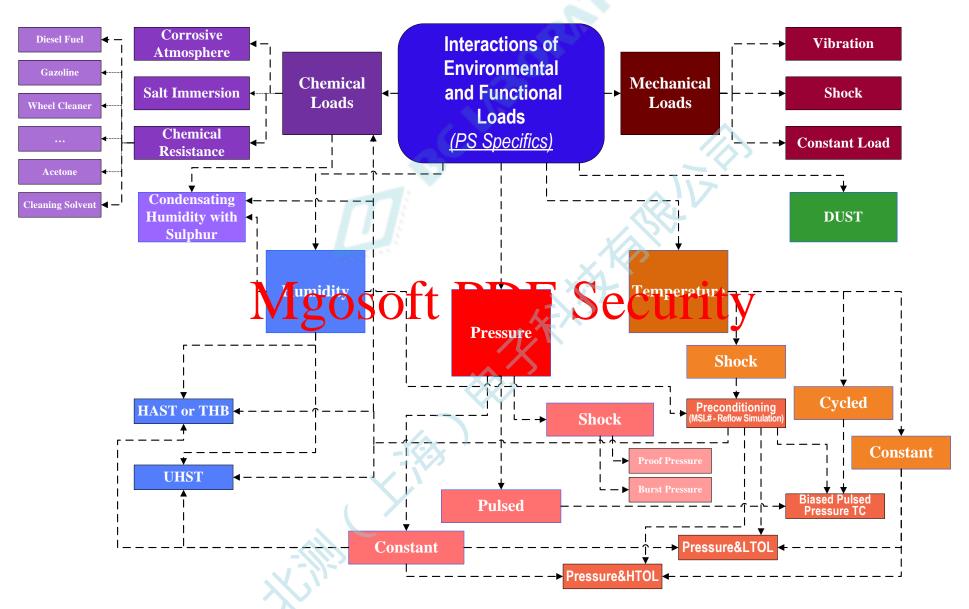


Figure 1: Basis of Determination of Qualification Tests

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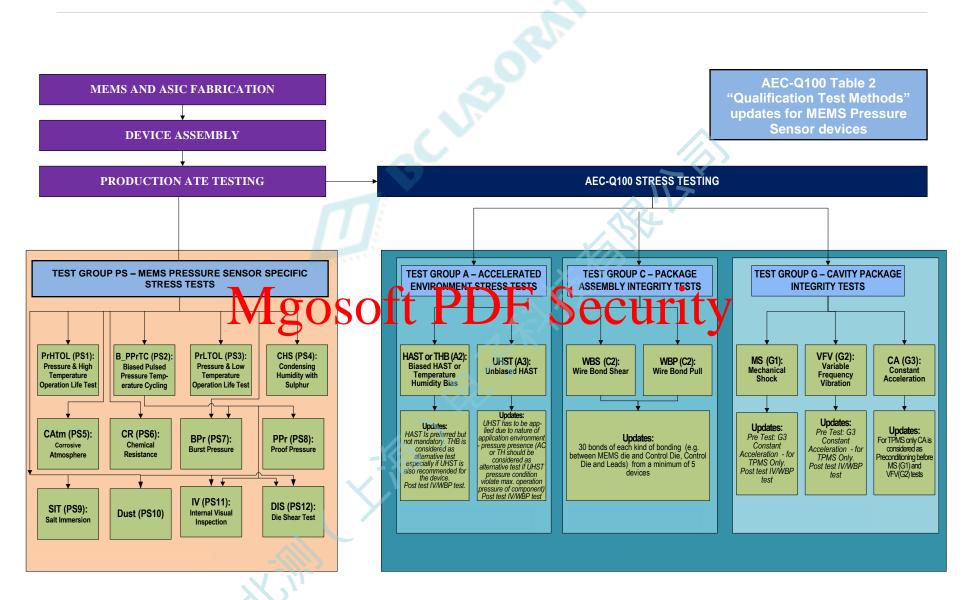


Figure 2: MEMS Pressure Sensor Device Qualification Test Flow

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Table 2A: MEMS Pressure Sensor Specific Qualification Test Methods

Note: AEC-Q100 shall be used to qualify the active circuitry contained within the MEMS pressure sensor device, as well as package integrity for the active circuitry. The following tests are specific to MEMS pressure sensor technology and package integrity for the MEMS technology. It is to be used in conjunction with AEC-Q100, rather than in lieu of.

	7	ΓEST	GROU	P PS –	MEMS P	RESSU	RE SENSOI	R SPECIFIC STRESS TESTS	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**) /LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Pressure & High Temperature Operating Life Test	PrHTOL	PS1	M	77 go	³ Sof	o Fails	Customer tailored plus JEDEC JESD22- A108	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1. HTOL per AEC-Q100 Test #B1 requirements taking into account the added MEMS grades: Grade 0A: +165°C Ta for 1000 hours Grade 0B: +175°C Ta for 1000 hours Pressure condition: maximum operation pressure, P _{max(op)} , according to MEMS device pressure range TEST before and after PrHTOL at room, cold, and hot temperature (in that order). Continuous monitoring of pressure sensor output signal is economical (In the cold of the col	Bulk die or diffusion defects, film stability and ionic contamination surface-charge spreading, mechanical creep, membrane fatigue, para- metric stability
Biased Pulsed Pressure Temperature Cycling	B_PPrTC	PS2		77	3	0 Fails	Customer tailored plus JEDEC JESD22- A104	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 TC per AEC-Q100 Test #A4 requirements taking into account the added MEMS grades.: Grade 0A: -55°C to +165°C for 2000 cycles or equivalent Grade 0B: -55°C to +175°C for 2000 cycles or equivalent Pressure cycling: fp=0.5 Hz in minimum operating pressure, Pmin(op), and maximum operating pressure, Pmax(op), pressure range (pressure rise and fall time should correspond to pressure mission profile from data sheet or to be adjusted according to application condition) Voltage condition: Vcc (max) at which dc and ac parameters are guaranteed TEST before and after B_PPrTC at cold and hot temperature. Continuous monitoring of pressure sensor output signal is required. Post-Test: IV (PS11) and WBP (C2) test for 5 devices; DIS (PS12) test for 5 parts; Burst Pressure Test (PS7) and Proof Pressure Test (PS8) for one lot. (B_PPrTC replaces AEC-Q100 Test #A4 TC if supplier can maintain appropriate transition time between hot and cold chamber)	Wire bond, wire, die bond, gel aeration, package failures, surface-charge spreading, volumetric gel changes, mechanical creep, membrane fatigue, parametric stability

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Table 2A: MEMS Pressure Sensor Specific Qualification Test Methods (continued)

	TEST G	ROUF	PPS-	MEMS	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Pressure & Low Temperature Operating Life Test	PrLTOL	PS3	G	77	1	0 Fails	MIL-STD-883 Method 1005.9	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 LTOL per MIL-STD-883 M1005.9 requirements. 1000 hours at minimum operating temperature, T _{min(op)} Pressure condition: maximum operation pressure, P _{max(op)} , according to MEMS device pressure range TEST before and after PrLTOL at room, hot, and cold temperature. Continuous monitoring of pressure sensor output signal is required.	Bulk die defects or diffusion defects, mechanical creep, membrane fatigue, parametric stability
Condensing Humidity with Sulphur (can be also testing in a saturated atmosphere in the presence of sulphur dioxide)	снѕ	PS4	M	g 0	sof	t°P	DN 10018	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 Bias Cycling condition: Vddmax, 1 hour ON, 1 hour OFF Test Cycle condition: 10 Cycles (1 cycle per 24hrs) according to DIN-50018 Sulphur condition: Concentration of SO2 at the beginning of acro lest cycle = 0.33 as per entage to volume T.S. before a ter C.IS. It room/temperature. Post-Test: IV (PS11) and WBF (C2) test for 5 devices. * Note: Certain applications may require modified test conditions.	Corrosion, wire bond, wire, contamination, volumetric gel changes, parametric stability
Corrosive Atmosphere	CAtm	PS5	G	10	111	0 Fails	EN 60068-2-60/ Method 4	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 Temperature condition: +25°C Humidity condition: 75% Flow Rate: 1m³/h Gases: SO2 at 0.20ppm; H2S at 0.01ppm; NO2 at 0.20ppm; Cl2 at 0.01ppm Duration: 14 days TEST before and after CAtm at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices. * Note: Certain applications may require modified test conditions.	Gel swelling, volumetric gel changes, corrosion, wire bond, wire, contamination parametric stability
Chemical Resistance (can be also solvent immersion)	CR	PS6	G	5 per chemical	1	0 Fails	Customer tailored plus ISO 16750-5		Gel swelling, volumetric gel changes, corrosion, wire bond, wire, contamination, parametric stability

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Table 2A: MEMS Pressure Sensor Specific Qualification Test Methods (continued)

	TEST G	ROUE	PPS-	MEMS	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Burst Pressure	BPr	PS7	M	15	sof	0 Fails 5xPfull-scale	Customer tailored	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Burst Pressure: the maximum pressure that may be applied to the sensor without a catastrophic failure. Temperature condition: maximum operating temperature, Tmax(op) Pressure condition: 5 x Pfull-scale = 5 x {Pmax(op)-Pmin(op)} Duration: 10 minutes, 1 time For Relative Pressure Sensors, apply pressure from back and front sides (i.e., perform Front-Side Burst Pressure Test and Back-Side Burst Pressure Test). Due to the destructive nature of the test, separate devices must be used for each test. Device shall be classified according to the maximum withstand pressure level. Devices should be stepped in pressure at 15 full-scale in the maximum of the stepped in the stepped states and the stepped states are states and the stepped states and the stepped states are states and the stepped states are states and the stepped states and the stepped states are states and the stepped states are states and the stepped states are states and the states are states are states and the states are states and the states are states are states and the states are states are states are states a	Diaphragm fracture, adhesive
Proof Pressure	PPr	PS8		15	3	0 Fails 3xP _{full} -scale	Customer tailored	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis. Proof Pressure: the maximum pressure that may be applied to the sensor without causing a change in performance with respect to the specifications (i.e., pressure that a sensor can routinely see without a permanent change in the output). Temperature condition: maximum operating temperature, Tmax(op) Pressure condition: 3 x Pfull-scale = 3 x [Pmax(op)-Pmin(op)] Duration: 10 minutes, 10 times For Relative Pressure Sensors, apply pressure from back and front sides (i.e., perform Front-Side Proof Pressure Test and Back-Side Proof Pressure Test). Device shall be classified according to the maximum withstand pressure level. Devices should be stepped in pressure at 0.5 x Pfull-scale increments. Device levels < 3 x Pfull-scale shall be documented in the supplier datasheet. TEST before and after PPr at room temperature. * Note: Certain applications may require modified test conditions.	or cohesive failure of die attach

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Table 2A: MEMS Pressure Sensor Specific Qualification Test Methods (continued)

	TEST G	ROU	PPS-	MEMS	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Salt Immersion Test	SIT	PS9	G	15	1	0 Fails	MIL-STD-883 Method 1002	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 Test conditions: 5 cycles of immersion between DI water at 65±3°C (60 min. dwell) and saturated salt water at 0±3°C (60 min. dwell) with 10 sec maximum transfer time. Immerse in DI water for 10 sec after the 5 cycles TEST before and after SIT at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices. * Note: Certain applications may require modified test conditions.	Package failure, corrosion, contamination .
Dust	DST	PS10	G	15	1	0 Fails	MIL-STD-202G Method 110A	Test conditions according to mission profile (protection class, if any) TEST before and after DST at room temperature. * Note: Certain applications may require modified test conditions.	Dust contamination
Internal Visual Inspection	IV	PS11	M	2 50	sof	t ₀ Fa	MIL S D-883 Me no 2013	Internal Visual Inspection for virgin-parts and post PS2, PS4, PS4 PS4 A1, 13, 61, and G2 tests.	
Die Shear Test	DIS	PS12	G	5	3	C _{PK} >1.67 or 0 Fails after B_PPrTC (PS2)	MIL-STD-883- Method 2019	MEMS Pressure Sensor Die Siter Test conditions: DIS is not required for wafer bonding. It should be applied to the die of the pressure sensing element integrated with the interface chip, or in case of stacked die or side-by-side die design, applied to the pressure sensing element.	

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Table 2B: AEC-Q100 Qualification Test Methods Updated for MEMS Pressure Sensor Devices

(AEC-Q100 Table 2 tests updated to address MEMS pressure sensor device failure mechanisms)

		U	PDATE	D TEST G	ROUP A	– ACCELE	ERATED ENV	IRONMENT STRESS TESTS	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Biased HAST or Temperature- Humidity-Bias	HAST or THB	A2		77	3	0 Fails	JEDEC JESD22-A110 or JESD22-A101	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 before HAST (130°C/85%RH for 96 hours, or 110°C/85%RH for 264 hours), or THB (85°C/85%RH for 1000 hours) TEST before and after HAST (or THB) at room and hot temperature. HAST is preferred but not mandatory. THB is considered an alternate test, especially if UHST is also performed for the device. Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Shift from ionic effect, moisture ingress, wire bond, package failure, gel swelling, parametric stability.
Unbiased HAST or Autoclave or Temperature- Humidity without Bias	UHST or AC or TH	А3	G	go "	sof	t P	JEDEC JESD22-A118 or JESD22-A102 or JESD22-A110	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 before unbiased HAST (130°C/85%RH for 96 hours, or 110°C/85%RH for 264 hours) or the special conditions of AC (221°C/15pkig for 96 hours) or TH (85°C/85%RH for 1000 hours). TEST before and after UHST (or AC or TH) at room temperature. Unbiased HAST shall be applied for MEMS pressure sensor devices due to nature of the application environment (i.e., pressure presence). AC should be considered an alternate test if HAST pressure conditions violate the device maximum operation pressure. TH should be considered an alternate test for packages sensitive to high temperatures and pressure. Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Wire bond, package failure, gel swelling, parametric stability.
			UPDA	TED TES	T GROUP	C - PAC	KAGE ASSEI	MBLY INTEGRITY TESTS	
Wire Bond Shear	WBS	C1	G	bonding (e	f each kind of e.g. between		AEC Q100-001 AEC Q003	See additional requirements for test C1 and C2 in Table 2 of AEC-Q100.	
Wire Bond Pull	WBP	C2	G	control die from a mi	nd control die e and leads) nimum of 5 vices	C _{PK} >1.67 or 0 fails after TC (test #A4)		Perform WBS test for virgin devices. Perform WBP test for virgin devices and post PS2, PS4, PS6, PS8, PS9, A2, A3, G1, and G2 tests.	

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Table 2B: AEC-Q100 Qualification Test Methods Updated for MEMS Pressure Sensor Devices (continued)

(AEC-Q100 Table 2 tests updated to address MEMS pressure sensor device failure mechanisms)

			UPD	ATED TE	ST GRO	JP G – CA	VITY PACKA	GE INTEGRITY TESTS	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Mechanical Shock	MS	G1	G	39	3	0 Fails	JEDEC JESD22-B110	Grade M1: Test conditions: 5 pulses in both directions of each axis, 0.3 ms duration, 6000 g peak acceleration Grade M2: Pre-Test: Constant Acceleration (CA) per Test #G3 below Test conditions: 10 pulses in both directions of each axis, 0.3 ms duration, 6000 g peak acceleration Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location) TEST before and after MS at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	
Variable Frequency Vibration	VFV	G2	G	go	sof	t P	DF JEDEC JESD22-B103	Grade M1: Test conditions: Per AEC-Q100 (50 g, 20Hz to 2kHz), stress shall be applied to each of three mutually percend but in 2es in plut and minus directions. Pre-Test: Constant Acceleration (CA) per Test #G3 below Test conditions: Per AEC-Q100 (50 g, 10Hz to 2kHz in 1 hour), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions. Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location). TEST before and after VFV at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Diaphragm fracture, package failure, die and wire bonds.
Constant Acceleration	CA	G3	G	39 (78 for TPMS only)	-13	0 Fails	MIL-STD-883 Method 2001	Grade M1: Test conditions: Per AEC-Q100 (2000 g for 1 min), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions Grade M2: Test conditions: Per AEC-Q100 (2500 g for 1 hour), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location) TEST before and after CA at room temperature.	

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Legend for Tables 2A and 2B

Notes:

- ** Sample size per life tests at bare die delivery. In case of bare die delivery (e.g., piezo-resistive pressure sensing element), test samples must be mounted on a "test substrate" or in ceramic packaging. Optional recommendation is joint qualification where user sub processes are implemented with reduced sample sizes per agreement between user and supplier.
- **G** Generic data allowed. See AEC-Q100, Section 2.3 and Appendix 1 of this document.
- # Reference Number for the particular test.

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Table 3: Process Change Qualification Guidelines for the Selection of Tests for MEMS Pressure Sensor Devices

- Temperature Humidity Bias / HAST C4 Physical Dimensions Autoclave / Unbiased HAST
- Temperature Cycling
- Power Temperature Cycling
 High Temperature Storage Life
 High Temperature Operating Life
- B2 B3 Early Life Failure Rate NVM Endurance, Data Retention
- C1 C2 C3 Wire Bond Shear
- Wire Bond Pull Solderability
- Solder Ball Shear
- Lead Integrity C7 Bump Shear Die Pull / Peeling C8 Lid Pull
- Electromigration
 Time Dependent Dielectric D2 . Breakdown
- D3 Hot Carrier Injection
- **Negative Bias Temperature** Instability Stress Migration **Human Body Model ESD**
- Charged Device Model ESD Latch-up **Electrical Distribution E7** Characterization Electromagnetic Compatibility

F3

- F10 Short Circuit Characterization E11 Soft Error Rate
- E12 Pb-Free G1-G4 Mechanical Series G5 Package Drop G6 Lid Torque G7 Die Shear
- Internal Water Vapor PS₁ Pressure & High Temperature Operating Life Biased Pulsed Pressure
 - **Temperature Cycling**
- PS3 Pressure & Low Temperature
- Operating Life Condensing Humidity with Sulphur
- PS₅ Corrosive Atmosphere PS6 Chemical Resistance
- **Burst Pressure** Proof Pressure Salt Immersion PS8 PS9
- PS10 PS11 Internal Visual Inspection PS12 Die Shear

Note: A letter or "•" indicates that performance of that stress test should be considered for the appropriate process change. Reason for not performing a considered test should be given in the qualification plan or results.

Table 2 Test #	A2	13	44	15	A6	B1	B2	B3	ပ	и	ខ	ŧ 'n	3 4	2 5	, ø	2 0	3 5	D2	83	7	: 52	E 2	83	4	E5	E7	E3	10	E11	7 7	5 5	3 9	G7	88	PS1	PS2	PS3	PS4	PS5	PS6	PS7	PS8	PS9	PS10	PS11	PS12
Test Abbreviation	THB		/ J.		HTSL /				WBS		SD			TSB				g				L				R			SER	2) SC	w	PrHTOL	B_PPrTC F			CAtm		BPr	Pr		DST		sia sia
DESIGN	-	_				_		ш,	2	2	0) 0			9								/ 1 -		<u> </u>		-	. ш.	0)	<u> </u>	<u> </u>	- 1 -		<u> </u>	<u> </u>	-						-		107			Ť
Active Element Design		•	•	М		•	•	DJ				Ī		9			D	D	D	D	D	•	•	•	•	•	•	•	•		F	:			•	•									П	П
Circuit Rerouting			Α	М						1							T					•	•	•	•	•	•	•		T	T												Ħ		\Box	_
Wafer Dimension/Thickness (including Pressure Sensor Membrane)			Е	М		•	•		Е	Е	111									•	,	Е	Е	Е	•					5	3				•	E	E				Е	Е			•	•
MEMS (Pressure Sensing Element) Design Change	•																									•				•	•				•	•					•	•				
WAFER FAB																																	abla													
Lithography	•		•	М		•	G		•	•										•	•				•					T	di				•	•										
Die Shrink	•	•		М		•	•	DJ									•	•	•	•	•	•	•	•	•	•	•	•	•		V	7			•										П	
Diffusion/Doping				М		•	G													•	•	•	•	•	•	•				V					•										П	
Polysilicon			•	М		•		DJ												•	,	•	•	•	•	•			7						•	•									П	_
Metallization/Via/Contacts	•	•	•	М		•	r		•	•			J.	Γ	Ī		1	1	Г	1	1		1		*	•		•			ľ	. 4		_	•	•									П	
Passivation/Oxide/Interlevel Dielectric	К	К	•	М		/ •]	GΝ	23	K	•	S	()	I	t		F	•	ŀ	J	1	ŀ	•	٠)	E			U			JŢ		/	•	•		к	К	K			К	K	κ	
Backside Operation			•	М		•			7				T		T							М	М	•	1	•				ŀ	1		7		•	•							П		П	_
FAB Site Transfer	•	•	•	М		•	•	J	•	•							•	•	•	•	•		•	•	•					H	1		Н		•	•	•	•	•	•	•	•	•	•	•	•
MEMS (Pressure Sensing Element) Specific Process	•																				1		K	T	•					•	•				•	•					•	•				
WAFER BUMPING	•	•		•	•	•				•								•			X			•		•								•		•		•				•				
Redistribution Layer	•	•	•	М	•									•	•	•	•			1	\Box				•											•									П	
Under Bump Metal	•	•	•	М	•									•	•	•									•											•									П	
Bump Material	•	•	•	М	•									•	•	•									•											•									П	_
Bump Site Transfer	•	•	•	М	•									•	•	•	•								•											•									П	
ASSEMBLY	•			•		•			•	•								•			•	•		•		•		•								•		•	•							
Die Overcoat/ Underfill	•	•	•	М	•	•						1		1															•					Н	•	•	•	•	•	•	•	•	•	•	•	•
Leadframe Plating	•	•	•	М	•					С	•		•	3	X														ı				Н					•	•	•			П			•
Bump Material/Metal System	•	•	•	М	•	•					•	•	1																• 1						•	•		•	•	•			П			
Leadframe Material		•	•	М	•					•	•	•	•	•														•	I	L	1		Н			•		•	•	•						•
Leadframe Dimension		•	•	М							•		•	•														•		L	1					•		•	•	•						•
Wire Bonding	S	•	•	Q	•				•	•															М			•		H	1					•		•	•	•			•			•
Die Scribe/Separate	•	•	•	М																																•										•
Die Preparation/Clean	•	•		М		•			•	•																							Н			•										•
Package Marking											В																																			
Die Attach	•	•	•	М		•	1	7																	•			•	Ī	L H	1		Н	Н	•	•		•	•	•	•	•	\square		ш	•
Molding Compound	•	• 1	•	М	_	•	•			_	•		•																• 1						•	•			•	•					Ш	•
Molding Process	•	•	•	М	_	•					•		•																I						•	•			•	•					Щ	•
Hermetic Sealing		Н	Н	7	Н						F	1	F	_																ŀ	_	Н	_	Н					<u> </u>						Ш	
New Package	•	•	•	М	_	•	•		•	•	•		_	•								•	•		•			•		L	_		Н	Н	•	•	•	•	•	•	•	•	•	•	•	•
Substrate/Interposer	•	•	•			•			•	•		1																		L F			Н	Н	•	•									Ш	
Assembly Site Transfer	•	•	•	М	1	•	•		•	•	•	• 1	•	•									1		•				1	L H	1		Н	Н	•	•	•	•	•	•	•	•	•	•	•	•

- Only for peripheral routing
- For symbol rework, new cure time, temp
- If bond to leadfinger
- Design rule change

- Thickness only
- MEMS element only
 Only from non-100% burned-in parts G
- Hermetic only

- EPROM or E²PROM
- Passivation only
- For Pb-free devices only For devices requiring PTC
- Passivation and gate oxide
- Wire diameter decrease MEMS Pressure Sensors
- For Solder Ball SMD only

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Appendix 1: Definition of a MEMS Pressure Sensor Product Qualification Family

MEMS Pressure Sensor product qualification family shall be compliant to Appendix 1 of AEC-Q100 with additional requirements specific to MEMS Pressure Sensor devices as defined below:

A1.1 Product

- i. Specified MEMS Operating Pressure Range
- Specified MEMS Operating Mechanical Condition (e.g., general Pressure Sensor, rim or tire mounted TPMS)
- k. Specified MEMS Operating Environmental Condition (e.g., details of expected harsh operating environment)

A1.2 Fab Process

b. Wafer Fab Process

- MEMS structure and material
- MEMS silicon cap bonding process and bonding materials
- MEMS internal atmosphere composition

A1.3 Assembly Process - Plastic, Ceramic, or Flip-Chip BGA

b. **Assembly Process**

MEMS sensor overcoat (e.g., silicone gel)

Table A1.1 MEMS Part Qualification/Requalification Lot Requirements (see AEC-Q100 with additional requirements as shown below)

Part Information	Lot Requirements for Qualification
New MEMS design and no applicable generic data.	Lot and sample size requirements AEC-Q100 Table 2 and Tables 2A/2B of this specification.
Generic data available for the MEMS design, but in a different package.	Only MEMS device specific tests as defined in Section 4.2 are required. Lot and sample size requirements per AEC-Q100 Table 2, and Tables 2A/2B of this specification for the required tests.
Same MEMS design and package, but new circuit or IC (with similar geometry).	Review Table 3 (both AEC-Q100 and this specification) to determine which tests from AEC-Q100 Table 2 and Tables 2A/2B of this specification should be considered.
MEMS design change, MEMS fabrication process change, or package change.	Review Table 3 (both AEC-Q100 and this specification) to determine which tests from AEC-Q100 Table 2 and Tables 2A/2B of this specification should be considered.

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Appendix 2: Q103-002 Certification of Design, Construction and Qualification

Supplier Name: Date:

The following information is required to identify a device that has met the requirements of AEC-Q103-002. Submission of the required data in the format shown below is optional. **All entries must be completed; if a particular item does not apply, enter "Not Applicable".** This template can be downloaded from the AEC website at http://www.aecouncil.com.

This template is available as a stand-alone document.

Item Name	Supplier Response
User's Part Number:	
Supplier's Part Number/Data Sheet:	
3. Device Description:	
4.1. Control Wafer/Die Fab Location & Process	
ID:	
a. Facility name/plant #: b. Street address:	
c. Country:	
4.2. MEMS Wafer/Die Fab Location & Process	
ID:	^ I\/\frac{1}{2}
a. Facility name/plant #:	NO.
b. Street address:	
c. Country: 4.3. Cap Wafer/Die Fab Location & Process ID:	****
a. Facility am /plant #:	DE OVER 14
a. Facility pant /plant #: b. Street advices OSOFT P	DE Security
5: 55 a.m. j.	of Security
4.4. Cap Wafer to MEMS Wafer Bonding	ÆX'
Location & Process ID:	
a. Facility name/plant #:b. Street address:	/ / / / / / / / / /
c. Country:	
5.1. Control Wafer Probe Location:	
a. Facility name/plant #:	
b. Street address:	
c. Country: 5.2. MEMS Wafer Probe Location:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
5.3. Bonded Wafer Probe Location:	
a. Facility name/plant #:	
b. Street address:c. Country:	
6. Assembly Location & Process ID:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
7. Final Quality Control A (Test) Location:	
a. Facility name/plant #:	
b. Street address:c. Country:	
o. Odditiy.	

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8.1. Control Wafer/Die:	4			
a. Wafer size:	A			
b. Die family:				
c. Die mask set revision & name:				1
d. Die photo:	See attached	Not av	ailable _	
8.2. MEMS Wafer/Die:	12/1			
a. Wafer size:				
b. Die family:				
c. Die mask set revision & name:	0	NI-4-	ما ما مالا من	
d. Die photo:	See attached	NOT a	vailable	
9.1. Control Wafer/Die Technology Description:				
a. Wafer/Die process technology:				
b. Die channel length:c. Die gate length:				
d. Die supplier process ID (Mask #):				
e. Number of transistors or gates:				
f. Number of mask steps:				
9.2. MEMS Wafer/Die Technology Description:				
a. Wafer/Die process technology:				
b. Sensor length x width x depth:			- Y	
c. Sensor anti-stiction coating:			117	
d. Die supplier process ID (Mask #):			1/	
e. Number of sensor detection elements				
(comb/fingers cells, pressure-sensing cells,		V/2/A		
thermos cells):	X			
f. Number of mask steps:			,	
9.3. Cap to MEMEM ale For one Technolog	HAP		1 T T T	
Description:		UUI J	LUY	
a. Bonding process technology:	,=\·)'			
b. MEMS cavity gas atmosphere after	**			
bonding.				
bonding: c. MEMS cavity pressure range after	/X			
	X			
 c. MEMS cavity pressure range after 	Control Die	MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding:	Control Die	MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions:	Control Die	MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width:	Control Die	MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length:	Control Die	MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness:			ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness:	Control Die Capped MEMS Wa		ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding:			ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process			ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description:			ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process			ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description:				Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness:	Capped MEMS Wa	fer		
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization:	Capped MEMS Wa	fer		
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s):	Capped MEMS Wa	fer		
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present):	Capped MEMS Wa	fer		
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer):	Capped MEMS Wa	fer	ie	
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers:	Capped MEMS Wa	fer MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers: b. Die passivation material(s):	Capped MEMS Wa	fer MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers: b. Die passivation material(s): c. Thickness(es) & tolerances:	Capped MEMS Wa	fer MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers: b. Die passivation material(s):	Capped MEMS Wa	fer MEMS D	ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers: b. Die passivation material(s): c. Thickness(es) & tolerances: d. MEMS Anti-stiction Coating:	Capped MEMS Wa	fer MEMS D	ie ie	Cap Die
c. MEMS cavity pressure range after bonding: 10.1. Die Dimensions: a. Die width: b. Die length: c. Die thickness (finished): d. Membrane Thickness: 10.2. Capped MEMS Thickness: a. After bonding: b. Bonded wafer thinning process description: c. Finished Capped MEMS die thickness: 11. Die Metallization: a. Die metallization material(s): b. Number of layers: c. Thickness (per layer): d. % of alloys (if present): 12. Die Passivation: a. Number of passivation layers: b. Die passivation material(s): c. Thickness(es) & tolerances:	Capped MEMS Wa	fer MEMS D	ie	Cap Die

14. Die	e Cross-Section Photo/Drawing:	Control Die	MEMS Die	Cap Die
		See attached Not available	See attached Not available	See attached Not available
	e Prep Backside:	Control Die	MEMS Die	Cap Die
a.				
b. C.	Die metallization: Thickness(es) & tolerances:			
0.	Trickness(es) & tolerances.	3		
16 Die	e Separation Method:	Control Die	MEMS Die	Bonded MEMS
	Kerf width (μm):	Control Dic	WEWO DIC	Die
b.	Kerf depth (if not 100% saw):			
C.	Saw method:			
		Single□ Dual□	Single☐ Dual☐	Single□ Dual□
17. Die	e Attach:	Control Die	MEMS	Die
a.				
	Die attach method:	0	0	
C.	Die placement diagram:	See attached Not available	See atta Not avai	
18. Pa	ckage:	110t available	Horavar	
a.	Type of package (e.g., plastic, ceramic,		W.	
	unpackaged):		∇D_{Λ}	
	Ball/lead count:			
C.	JEDEC designation (e.g., MS029,	DE CO	X	
d.	MS034 ttg.: COSOTT Lead (Pb) free Co. Morrogenous	DF Se	curity	
u.	material):	Yes No		
e.		See attached	□ Not available □	7
19.1.	Mold Compound:		_	_
a.	Mold compound supplier & ID:	/ X		
b.	Mold compound type:		_	
C.	Flammability rating:	UL 94 V1 🗌	UL 94 V0	
d.	Fire Retardant type/composition:			
e.	Tg (glass transition temperature)(°C):	CTE1 (above Tg) -	: CTE2 (be	low Ta) –
f. 19.2.	CTE (above & below Tg)(ppm/°C): Package Material Used Before or After		and c shall supply	
	Over MEMS or Capped MEMS Die:	coverage drawing v		WILING IIIalellal
a.	Material type and ID:	coverage arawing t		
b.	Minimum material coverage:	See a	attached N	ot available
C.	Maximum material coverage:	See a	attached N	ot available
20.1 <u>.</u>	Die to Leadframe Wire Bond:			
a.	Wire bond material:			
b.	Wire bond diameter (mils):			
C.	Type of wire bond at die:			
d. e.	Type of wire bond at leadframe: Wire bonding diagram:	See attached	Not available	7
20.2.	Die to Die Wire Bond:		TYOL GVAIIADIE L	
a.	Wire bond material:			
b.	Wire bond diameter (mils):			
C.	Type of wire bond at Control die:			
d.	Type of wire bond at MEMS die:		_	_
e.	Wire bonding diagram:	See attached	Not available	

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21.	Lea	adframe (if applicable):	Control Die	MEMS Die
	a.	Paddle/flag material:		
	b.	Paddle/flag width (mils):		
	c.	Paddle/flag length (mils):	/ O *	
	d.	Paddle/flag plating composition:		
	e.	Paddle/flag plating thickness (μinch):		
	f.	Leadframe material:		
	g.	Leadframe bonding plating composition:		
		Leadframe bonding plating thickness	200	
		(μinch):		
	i.	External lead plating composition:	120	
	j.	External lead plating thickness (µinch):		
22.	Su	bstrate (if applicable):		
	a.			
	b.	Substrate thickness (mm):		
		Number of substrate metal layers:		
	d.	Plating composition of ball solderable		
		surface:		
	e.	Panel singulation method:		
	f.	Solder ball composition:		
	g.	Solder ball diameter (mils):		117
23.		packaged Die (if not packaged):		
	a.	Under Bump Metallurgy (UBM)		
		composition:		
		Thickness of UBM metal:		
		Bump amposition:	DE Coour	4
		Bump size. 9 0 S	Dr Secul	LL V
		ader Material (il applicable):	7 %	
25.	Th	ermal Resistance:	4-X	
	a.	θ _{JA} °C/W (approx):	1.8	
	b.	θ _{JC} °C/W (approx):		
	C.			
		techniques:		
26.		st circuits, bias levels, & operational	<u>_</u>	_
		nditions imposed during the supplier's life	See attached Not av	ailable 🗌
	and	d environmental tests:		
27.	Fa	ult Grade Coverage (%)	% Not digital	-
28.	Ма	ximum Process Exposure Conditions:	* Note: Temperatures are as	
			the plastic package body top	
	a.	MSL @ rated SnPb temperature:	at°C (SnPl	b)
	b.	MSL @ rated Pb-free temperature:	at°C (Pb-fi	ree)
	c.	Maximum dwell time @ maximum	·	
		process temperature:		

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Attachments:		Requirements:
Die Photo		1. A separate Certification of Design, Construction &
Package Outline Drawing		Qualification must be submitted for each P/N, wafer
Die Cross-Section Photo/Draw	/ing	fab, and assembly location.
Wire Bonding Diagram		 Design, Construction & Qualification shall be signed by the responsible individual at the supplier who can verify the above information is accurate and complete. Type name and sign below.
Die Placement Diagram		
MEMS material coverage draw dimensions Test Circuits, Bias Levels, & Conditions	ving with	
Completed by:	Date:	Certified by: Date:
Typed or Printed:		
Signature:		
Title:		-30

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Appendix 4: Minimum Requirements for MEMS Pressure Sensor Qualification Plans and Results

The following information is required as a minimum to identify a device that has met the requirements of AEC-Q103-002 (see Appendix Templates 4A and 4B). Submission of data in this format is optional. However, if these templates are not used, the supplier must ensure that each item on the template is adequately addressed. The templates can be downloaded from the AEC website at http://www.aecouncil.com.

A4.1 **Plans**

- 1. Part Identification: Customer P/N and supplier P/N.
- 2. Site or sites at which life testing will be conducted.
- 3. List of tests to be performed (e.g., JEDEC method, Q100 and Q103-002 method, MIL-STD method) along with conditions. Include specific temperature(s), humidity, and bias to be used.
- 4. Sample size and number of lots required.
- 5. Time intervals for end-points (e.g., after PC, 0 hour, 500 hour, 1000 hour).
- 6. Targeted start and finish dates for all tests and end-points.7. Supplier name and contact.
- 8. Submission date.
- 9. Material and functional details and test results of devices to be used as generic data for qualification. Include rationale for use of generic data.

A4.2 Results

All of above plus:

- 1. Date codes and lo codes of parts ested.
- Process identification.
- 3. Fab and assembly locations.
- 4. Mask number or designation.
- 5. Number of failures and number of devices tested for each test.
- 6. Failure analyses for all failures and corrective action reports to be submitted with results.

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Appendix Template 4A: AEC-Q103-002 Qualification Test Plan

		•	2 QUALIFICATIO	N TEST PLAN						
USER COMPANY:				DATE:						
USER P/N: TRACKING NUMBER:										
USER SPEC #:			USE	R COMPONENT ENGINEER:						
SUPPLIER COMPANY:			SUPPLIE	R MANUFACTURING SITES:						
SUPPLIER P/N:				PPAP SUBMISSION DATE:						
SUPPLIER FAMILY TYPE: REASON FOR QUALIFICATION:										
STRESS TEST	ABV	TEST#	TEST METHOD	Test Conditions/S.S. per Lot/# Lots (identify temp, RH, & bias)			RESULTS Fails/S.S./# lots			
Preconditioning	PC	A1	J-STD-020	Peak Reflow Temp. = Preconditioning used =	Min. N	MSL = 3	MSL =			
Temperature Humidity Bias or HAST	THB / HAST	A2	JESD22-A101/A110	1 Toochalloring about =	77	3				
Autoclave or Unbiased HAST	AC / UHST	А3	JESD22-A102/A118		77	3				
Temperature Cycle	TC	A4	JESD22-A104		77	3				
Power Temperature Cycling	PTC	A5	JESD22-A105		45	1				
High Temperature Storage Life	HTSL	A6	JESD22-A103		45	1				
High Temperature Operating Life	HTOL	B1	JESD22-A108		77	3				
Early Life Failure Rate	ELFR	B2	AEC Q100-008		800	3				
NVM Endurance, Data Retention, & Operational Life	EDR	В3	AEC Q100-005	. 11	77	3				
Wire Bond Shear	WBS	C1	AEC Q100-001		5	1				
Wire Bond Pull Strength	WBP	C2	MIL-STD-883 - 2011		5	1				
Solderability	SD	C3	JESD22-B102 J-STD-002D	8 hr steam aging prior to testing	15	1				
Physical Dimensions	PD	C4	JESD22-B100/B108	a V	10	3				
Solder Ball Shear	OF BEC	\(\sigma \)	ĀEC ⊋400 010 ─	ACIII11	T I 0	3				
Lead Integrity	540	C6	JESD22-B105	Deculit	5	1				
Electromigration	EM	D1	L'	-X->		•				
Time Dependent Dielectric Breakdown	TDDB	D2	A 1							
Hot Carrier Injection	HCI	D3								
Negative Bias Temperature Instability	NBTI	D4								
Stress Migration	SM	D5	XX.							
Pre- and Post-Stress Electrical Test	TEST	E1	Test to spec		,	All				
ESD - Human Body Model	HBM	E2	AEC Q100-002		See Te	st Method				
ESD - Charged Device Model	CDM	E3	AEC Q100-011		See Te	st Method				
Latch-Up	LU	E4	AEC Q100-004		6	1				
Electrical Distributions	ED	E5	AEC Q100-009		30	3				
Fault Grading	FG	E6	AEC-Q100-007							
Characterization	CHAR	E7	AEC Q003							
Electromagnetic Compatibility	EMC	E9	SAE J1752/3		1	1				
Short Circuit Characterization	SC	E10	AEC Q100-012		10	3				
Soft Error Rate	SER	E11	JESD89-1, -2, -3		3	1				
Lead Free	LF	E12	Q005		See Te	st Method				
Process Average Test	PAT	F1	AEC Q001							
Statistical Bin/Yield Analysis	SBA	F2	AEC Q002							
Mechanical Shock	MS	G1	JESD22-B110		39	3				
Variable Frequency Vibration	VFV	G2	JESD22-B103		39	3				
Constant Acceleration	CA	G3	MIL-STD-883 - 2001		39/78	3				
Gross/Fine Leak	GFL	G4	MIL-STD-883 - 1014		15	1				
Package Drop	DROP	G5			5	1				
Lid Torque	LT	G6	MIL-STD-883 - 2024		5	1				
Die Shear Strength	DS	G7	MIL-STD-883 - 2019		5	1				

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Appendix Template 4A: AEC-Q103-002 Qualification Test Plan (continued)

STRESS TEST	ABV	BV TEST# TEST METHOD		Test Conditions/S.S. per Lot/#	REQUIR	EMENTS	RESULTS
31KE33 1E31	ADV	IESI#	1E31 WETHOD	Lots (identify temp, RH, & bias)	S.S	# LOTS	Fails/S.S./# lots
Pressure & High Temperature Operating Life Test	PrHTOL	PS1	JESD22- A108	70.	77	3	
Biased Pulsed Pressure Temperature Cycling	B_PPrTC	PS2	JESD22- A104		77	3	
Pressure & Low Temperature Operating Life Test	PrLTOL	PS3	MIL-STD-883 - 1005		77	1	
Testing in a saturated atmosphere in the presence of sulfur dioxide	CHS	PS4	DIN 50018		45	1	
Corrosive Atmosphere	CAtm	PS5	EN 60068-2-60 / Method 4		10	1	
Chemical Resistance	CR	PS6	ISO 16750-5		Var (5xChemical)	1	
Burst Pressure	BPr	PS7			15	3	
Proof Pressure	PPr 🥟	PS8			15	3	
Salt Immersion Test	SIT	PS9	MIL-STD-883 - 1002		15	1	
Dust	DST	PS10	MIL-STD-202G - 110A		15	1	
Internal Visual Inspection	IV	PS11	MIL-STD-883 - 2013		5	3	
Die Shear Test	DIS	PS12	MIL-STD-883 - 2019		5	3	
Supplier:		•		Approved by: (User Engineer)	7		

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TEST = HOT

TEST = ROOM and

TEST = ROOM and

HOT TEST = ROOM,

COLD, and HOT TEST = ROOM and

HOT TEST = ROOM and

HOT

Cpk>1.5 and in SPC

>95% solder

coverage Cpk > 1.5 per

JESD95

rs efo e SBS

77

45

45

77

800

77

15

10

3

1

1

3

3

3

An appropriate time period

for each bonder to be used

1

3

Α4

Α5

A6

B1

B2

B3

C1

C2

C3

C4

C5

TC

PTC

HTSL

HTOL

ELFR

EDR

WBS

WBP

SD

PD

SBS

JESD22-A104

JESD22-A105

JESD22-A103

JESD22-A108

AEC Q100-008

AEC Q100-005

AEC Q100-001

MIL-STD-883 - 2011 JESD22-B102

J-STD-002D

JESD22-B100/B108

AEC Q 00-

Annendix Template 4B: AFC-Q103-002 Generic Data

bjective: Device: Cust PN: Maskset: Die Size:			Package: Fab/Assy/Test: Device Engr: Product Engr: Component Engr:				70		Qual Plan Ref # Date Prepared Prepared by Date Approved Approved by			
Test #	ABV	Q100 Test Conditions	End-Point Requirements	Sample Size/Lot		Total # Units	Part to be Qualified	Differences from Q100/Q103	Generic Family part A	Differences from Q100/Q103	Generic Family part B	Differences from Q100/Q103
A1	PC	J-STD-020	TEST = ROOM	All surface to A2	mount 2, A3, A							
A2	THB / HAST	JESD22-A101/A110	TEST = ROOM and HOT	77	3	231						
А3	AC /	JESD22-A102/A118	TEST = ROOM	77	3	231						

231

45

45

231

2400

231

15

30

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Appendix Template 4B: AEC-Q103-002 Generic Data (continued)

Test #	ABV	Q100 Test Conditions	End-Point Requirements	Sample Size/Lot	# of Lots	Total # Units	Part to be Qualified	Differences from Q100/Q103	Generic Family part A	Differences from Q100/Q103	Generic Family part B	Differences from Q100/Q103
PS1	PrHTOL	JESD22- A108	TEST = ROOM, COLD, and HOT	77	3	231						
PS2	B_PPrTC	JESD22- A104	TEST = COLD and HOT	77	3	231	20	,				
PS3	PrLTOL	MIL-STD-883 - 1005	TEST = ROOM, HOT and COLD	77	1	77	2					
PS4	CHS	DIN 50018	TEST = ROOM	45	1	45						
PS5	CAtm	EN 60068-2-60 / Method 4	TEST = ROOM	10	1	10						
PS6	CR	ISO 16750-5	TEST = ROOM	Var (5/Chemical)	1	Var (5/Chemical						
PS7	BPr		TEST = ROOM	15	3	45						
PS8	PPr		TEST = ROOM	15	3	45						
PS9	SIT	MIL-STD-883 - 1002	TEST = ROOM	15	1	15						
PS10	DST	MIL-STD-202G - 110A	TEST = ROOM	15	1	15						
PS11	IV	MIL-STD-883 - 2013		5	3	15						
PS12	DIS	MIL-STD-883 - 2019		5	3	15						

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
User Part Number		1	
Supplier Part Number			
	A1.1 Pi	roduct	
Product Functionality (e.g., Op- Amp, Regulator, Microprocessor, Logic – HC/TTL)		X	
Operating Supply Voltage Range(s) Specified MEMS Operating	and DD	E Coolini	₹ 7
Temperature Range	SOIL F D	r Seculi	· y
Specified MEMS Operating Frequency Range		Y-X,	
Specified MEMS Operating Pressure Range	Λ		
Specified MEMS Operating Mechanical Condition (e.g., general Pressure Sensor, rim or tire mounted TPMS)	(A)		
Specified MEMS Operating Environmental Condition (e.g., details of expected harsh operating environment)	-16x)		
Analog Design Library Cells (e.g., active circuit elements, passive circuit elements)	V:\V'		
Digital Design Library Cells (e.g., circuit blocks, IO modules, ESD cells)	Y		
Memory IP (e.g., cell structure, building block)			
Memory Type(s) & Size(s)			
Design Rules for Active Circuits under Pads			
Other Functional Characteristics (as defined by supplier)			

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Appendix Template 4B: AEC-Q103-002 Generic Data (continued)

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
Fait Attributes			Generic Failing Fait B
Wefer Feb Teck as Is an / a m	A1.2 Fab	Process	
Wafer Fab Technology (e.g., CMOS, NMOS, Bipolar)			
Circuit Element Feature Size (e.g.,			
layout design rules, die shrinks,		31	
contacts, gates, isolations)			
Substrate (e.g., orientation, doping,		9	
epi, wafer size)			
Maximum Number of Masks			
(supplier must show justification for waiving this requirement)			
Lithographic Process (e.g., contact			
vs. projection, E-beam vs. X-ray,			
photoresist polarity)			
Doping Process (e.g., diffusion vs.			
ion implantation)			
Gate Structure, Material & Process			
(e.g., polysilicon, metal, salicide,	Acceptance of the Control of the Con		
wet vs. dry etch) Polysilicon Material, Thickness	2000		
Range, & Number of Levels			
Oxidation Process & Thickness			
Range (e.g., gate & field oxides)			7
Interlevel Dielectric Material &		^	
Thickness Range		N/L	
Metallization Material, Thickness		///	
Range, & Maximum Number of			
Levels	_		
Passivation Process (e.g.	and DD	Γ Coordinate	
passivation oxide opening, Material, & Thickness kar ge	DSOH PIJ	r securi	V
Die Backside Preparation Process		DOCUIII	
& Metallization		1=X	
Wafer Fabrication Site	1	**	
MEMS Structure and Material		/	
MEMS Silicon Cap Bonding			
Process and Bonding Materials.	^		
MEMS Internal Atmosphere			
Composition			
	A1.3 Assembly Proces	s – Plastic or Ceramic	
Assembly Site			
Package Type (e.g., DIP, SOIC,			
QFP, PGA, PBGA)	- 3.77		
•			
Range of Paddle/Flag Size (maximum & minimum dimensions)			
Qualified for the Die Size/Aspect			
Ratio Under Consideration			
Worst Case Package (e.g.,			
package warpage due to CTE mismatch)			
Substrate Base Material (e.g.,			
PBGA)			
Leadframe Base Material			
Die Header / Thermal Pad Material			
Leadframe Plating Material &			
Process (internal & external to the			
package)			
Die Attach Material			

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Appendix Template 4B: AEC-Q103-002 Generic Data (continued)

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
Wire Bond Material & Diameter			
Wire Bond Method, Presence of Downbonds, & Process			
Plastic Mold Compound Material, Organic Substrate Material, or Ceramic Package Material		Bis	
Plastic Mold Compound Supplier/ID		7	
Solder Ball Metallization System (if applicable)	16		
Heatsink Type, Material, & Dimensions			
Die Preparation/Singulation			
MEMS sensor Overcoat: Material or Process (e.g., silicone gel)	П		

Note 1: Design Library cells need to follow guidelines for temperature ranges, voltage ranges, speed, performance, and power dissipation as defined in Appendix 1 of this document.

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Revision History

Rev # Date of change Brief summary listing affected sections

- March 1, 2019 Initial Release